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Hsu, et al.

**Serial No.: 09/739,139**

**Confirmation No.:**

Filed: December 18, 2000

**For: Integrated Multi-Step Gap Fill and All Feature Planarization for Conductive Materials**

Assistant Commissioner for Patents  
Washington, D.C. 20231

**Dear Sir:**

**CERTIFICATE OF MAILING**

37 CFR 1.8

I hereby certify that this correspondence is being deposited on May 16, 2001 with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231.

5/16/01  
Date

B. Todd Peterson  
Signature

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

The Applicants, and the Attorney who signs below on the basis of the information supplied by the inventor and the information in his file, submit herewith patents, publications, or other information of which they are aware, which may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

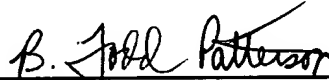
While the information submitted in this Supplemental Information Disclosure Statement may be material pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication, or other information referred to therein is prior art for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97, this Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other possibly material information as defined under 37 CFR § 1.56(a) exists.

The patents and/or publications submitted herewith are set forth on the attached Form PTO-1449.

If the sum of \$180.00 is due under 37 CFR § 1.17(p) pursuant to § 1.97, the Commissioner is hereby authorized to charge this fee, and any other fee necessary to make this submission timely, to the Deposit Account No. 20-0782/APPM/5614/BTP.

Respectfully submitted,



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